

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-22. (Canceled)

23. (New) A panel comprising:

a substratum;

a plurality of scan lines formed on the substratum;

a plurality of data lines intersecting with the plurality of scan lines wherein the plurality of data lines are formed on the substratum;

a plurality of pixels disposed at locations where the plurality of scan lines intersect with the plurality of data lines; and

a peripheral circuit formed on the substratum;

wherein the peripheral circuit includes first and second processes, and the first and second processes are controlled by means of event driving, and the first process requires the second process to forward data, and the second process forwards the data to the first process,

wherein the first process includes a first port, and the second process includes a second port, and the first and second ports are connected via a channel, and a req signal and an ack signal are sent or received between the first and second ports,

wherein the second port senses the req signal when the first port raises a level of the req signal from an L level to an H level, and the second port raises a level of the ack signal from the L level to the H level when sensing the req signal, and the first port senses a transition of the ack signal when the level of the ack signal is raised from the L level to the H level, and the first port lowers the level of the req signal from the H level to the L level.

24. (New) The panel according to claim 23, wherein the peripheral circuit includes thin film transistors.
25. (New) The panel according to claim 23, further comprising:
a scan line driver for outputting scanning signals on the plurality of scan lines wherein the scan line driver is formed on the substratum; and
a data line driver for outputting data signals on the plurality of data lines wherein the data line driver is formed on the substratum.
26. (New) The panel according to claim 23, wherein the substratum is a glass substratum.
27. (New) The panel according to claim 23, wherein the peripheral circuit renders an image displayed by the plurality of pixels.
28. (New) The panel according to claim 23, wherein each of the plurality of pixels includes a switching element.
29. (New) The panel according to claim 23, wherein the peripheral circuit includes a CPU.
30. (New) The panel according to claim 23, wherein the peripheral circuit includes a memory.
31. (New) The panel according to claim 23, wherein the peripheral circuit includes a rendezvous circuit, and the first port includes an encoder, and the second port includes a decoder, and data is sent from the first port to the second port, and the data received by the second port is input to the rendezvous circuit.
32. (New) A display device comprising the panel according to claim 23, wherein the panel is an organic EL panel.
33. (New) A display device comprising the panel according to claim 23, wherein the panel is a liquid crystal display panel.

- 34. (New) A display device comprising the panel according to claim 23, wherein the panel is an electrophoretic panel.
- 35. (New) An electronic device comprising the panel according to claim 23.
- 36. (New) An electronic device comprising the display device according to claim 32.